

Online Library Accelerating  
Test Validation And Debug Of  
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receiver test more than 1000 times  
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Testing of HSSIs has been a challenging  
topic because of signal integrity issues,  
long test time and the need of expensive

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instruments. Accelerating Test,  
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Accelerating Test, Validation and Debug  
of High Speed Serial Interfaces provides  
innovative test and debug Accelerating  
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## **Accelerating Test, Validation and Debug of High Speed ...**

The cost to testing the associated IPs is

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also on the rise. To mitigate this post silicon validation and debug challenge, design teams have resorted in applying on-chip debug strategy, more automated techniques for post-silicon test generation and pre-tapeout assertions for effective coverage/analysis.

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## **Accelerating Post-Silicon Debug and Test - SemiWiki**

Turning on GPU-based validation GBV can be forced on using the DirectX Control Panel (DXCPL) by forcing on the Direct3D 12 Debug Layer and additionally forcing on GPU-based validation (new tab in the control panel). Once enabled, GBV will remain enabled

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until the Direct3D 12 device is released.

## **GPU-based validation and the Direct3D 12 Debug Layer ...**

Session: Accelerating SOC Debug and Validation at Quality. ... In this presentation, we will share how these data availability problems affect test creation, test execution and failure

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